



TAOGLAS®



Datasheet

Part No:
AGVLBD258A

Description

Evaluation Board for GVLB258.A with TFM.110A

Features:

Optimum Active Electronics Design for Passive GNSS L1/L5 Antenna
Integrated TFM Module and Hybrid Coupler

Bands Covered:

- BeiDou (B1)
- GPS/QZSS (L1/L5)
- GLONASS (G1)
- Galileo (E1/E5a)

RoHS & Reach Compliant

1.	Introduction	3
2.	Specification	5
3.	Mechanical Drawing	7
4.	Integration Guide	8
5.	Antenna Characteristics	12
6.	Radiation Patterns	14
<hr/>		
	Changelog	19

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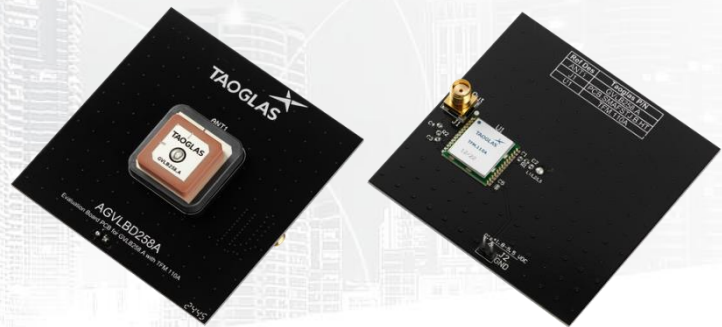
Ireland & USA
ISO 9001:2015
Certified



Taiwan
ISO 9001:2015
Certified



1. Introduction



Taoglas GNSS Reference Design Evaluation Boards

The Taoglas series of GNSS Antenna Reference Boards are designed to show the optimum component layout and make it easy for you to integrate the active electronics with the passive GNSS antenna on your device PCB.

Designing active GNSS electronics can be difficult, but utilising Taoglas' **TFM Series** of front end active electronics and hybrid couplers allow you to achieve the best possible positioning performance for your device. The Taoglas TFM module features a SAW/LNA/SAW/LNA topology in both the low and high band signal paths to prevent unwanted out-of-band interference from overdriving the GNSS LNAs or receiver.

Features and Benefits of utilising the TFM Modules.

Ease-of-integration – Single-package solution combines impedance matching, filter efficiency and low noise design for easy, drop-in use with any antenna or GNSS receiver

Low-noise System Design – Integrated pre-filters deliver exceptional out-of-band rejection across multiple band configurations and neighbouring interference to properly set noise figure

Dual-gain Stage Architecture – Cascaded LNAs, pre-filters and optimized impedance matching deliver sufficient gain to the GNSS receiver without signal-to-noise overload

Low-profile Form Factor – Small footprint and low-profile design saves valuable real estate without the need for external components and routing

Accelerated Development Cycles – 2+ years of development by antenna and RF design experts, delivering the highest levels of integration, manufacturability and robustness in a single package

View the full series here:

<https://www.taoglas.com/product-category/other-rf-components/front-end-modules/>

Where used, the Taoglas **HC125A** is a low profile, high performance, 3dB hybrid coupler designed for multi-feed Multiband GNSS applications. The HC125.A is ideal for applications where balanced power and low noise amplifiers are required. For more information on the HC125A, visit the product page [here](#).

Please visit the standard product page for the HP54510.A to download the integration files in Altium, 2D and 3D formats for your project by clicking [here](#).

For more information or to order samples, reach out to your regional Taoglas customer support team.

2. Specification

GNSS Frequency Bands					
GPS	L1 1575.42 MHz	L2 1227.6 MHz	L5 1176.45 MHz		
	■	□	■		
GLONASS	G1 1602 MHz	G2 1248 MHz	G3 1207 MHz		
	■	□	□		
Galileo	E1 1575.24 MHz	E5a 1176.45 MHz	E5b 1201.5 MHz	E6 1278.75 MHz	
	■	■	□	□	
BeiDou	B1C 1575.42 MHz	B1I 1561 MHz	B2a 1176.45 MHz	B2b 1207.14 MHz	B3 1268.52 MHz
	■	■	■	□	□
L-Band	L-Band 1542 MHz				
	□				
QZSS (Regional)	L1 1575.42 MHz	L2C 1227.6 MHz	L5 1176.45 MHz	L6 1278.75e6	
	■	□	■	□	
IRNSS (Regional)	L5 1176.45 MHz				
	■				
SBAS	L1/E1/B1 1575.42 MHz	L5/B2a/E5a 1176.45 MHz	G1 1602 MHz	G2 1248 MHz	G3 1207 MHz
	■	■	■	□	□



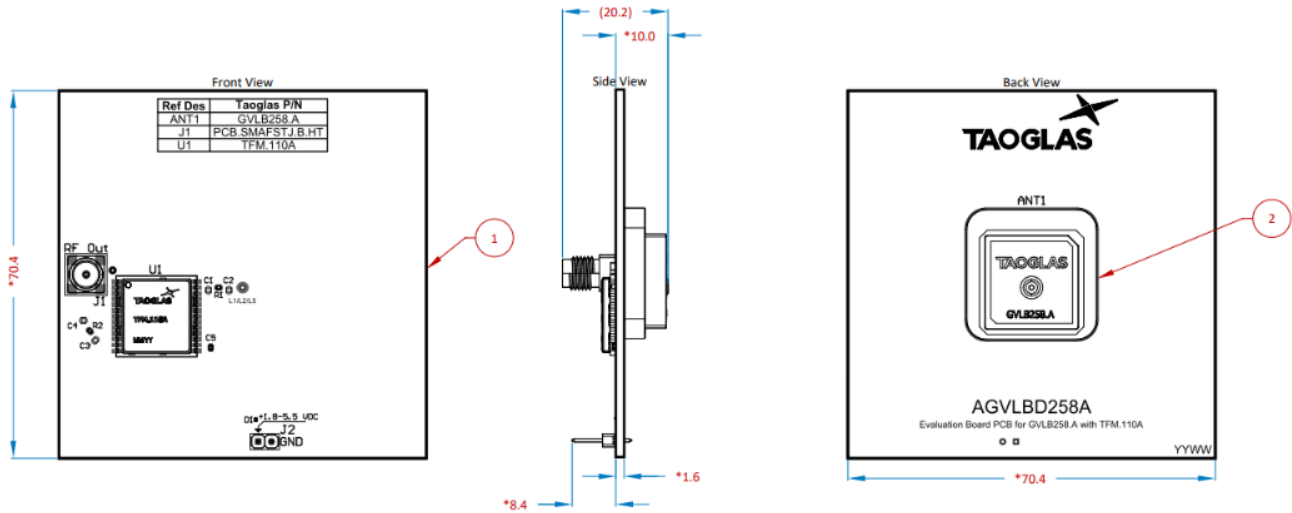
GNSS Bands and Constellations

GNSS Electrical				
Frequency (MHz)	1176.45	1561	1575.42	1603
Peak Gain (dBi)	32.71	30.86	30.21	29.43
Axial Ratio (dB)	14.47	10.8	7.87	17.58
PCO_x (cm)	0.56	0.78	0.68	0.57
PCO_y (cm)	-1.3	-2.2	-2.2	-2.32
PCV (cm)	3.18	1.68	1.82	2.14
Group Delay Mean (ns)	34.14	20.18	20.39	26.53
Polarization	RHCP			
Impedance	50 Ω			

Mechanical	
Dimensions	70.4 X 70.4 X 20.2mm
Material	Antenna: Ceramic Board: FR4
Connector	SMA(F)

Environmental	
Operation Temperature	-40°C to 85°C
Storage Temperature	-40°C to 85°C
Relative Humidity	Non-condensing 65°C 95% RH

3. Mechanical Drawing

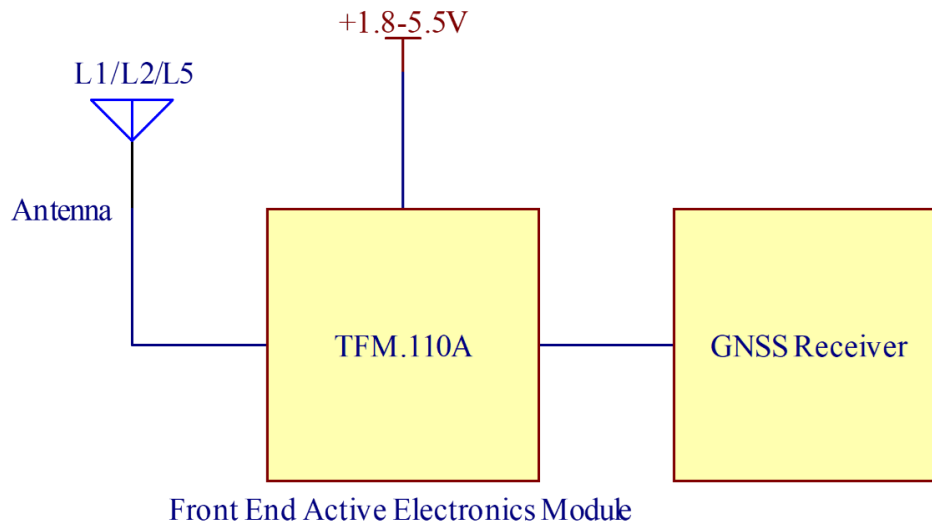


BOM Of Materials

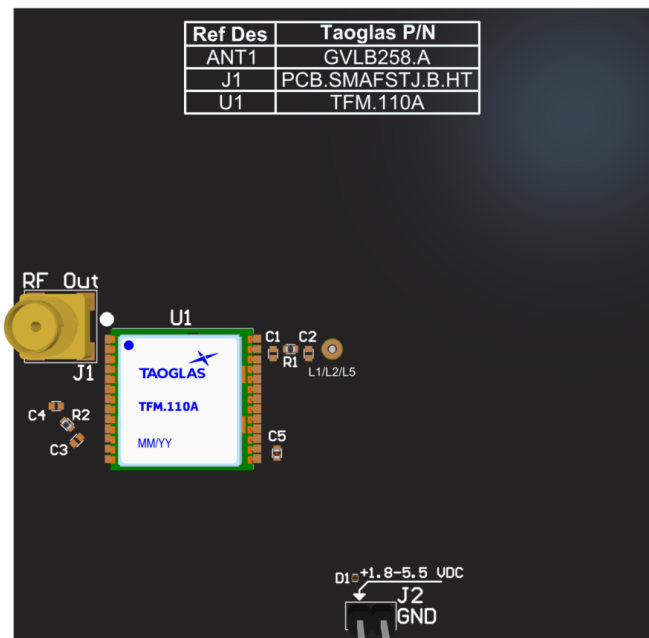
Line #	Description	Quantity	MFG	MFGPN
1	SMD_AGVLBD258A Dual Layer L70.4 W70.4 H1.6mm NP-140 35um Glossy Black	1	Taoglas	AGVLBD258A
2	GVLB258.A /PPK/ Antenna no packaging	1	Taoglas	GVLB258.A

4. Integration Guide

The following is an example on how to integrate the TFM.110A into a design. In this example, the GVLB258.A is used as the antenna. This antenna has one pin, which is used for the L1/L2/L5 bands. The TFM.110A is powered from a separate power DC supply (1.8V-5.5V). The output of the TFM.110A can then be fed to a relevant GNSS receiver module. Taoglas recommends using a minimum of 70x70mm ground plane (PCB) to ensure optimal performance.



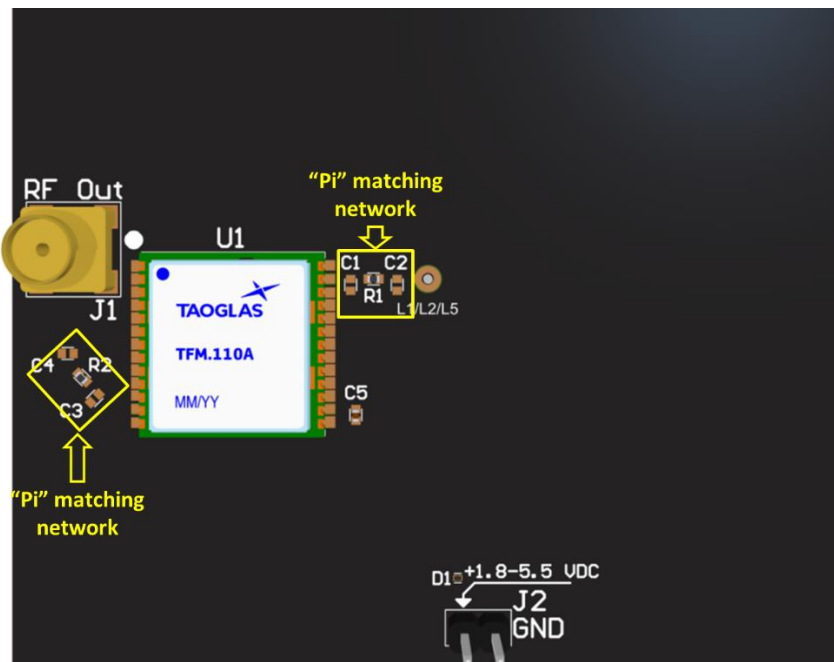
Block Diagram of integration of the TFM.110A



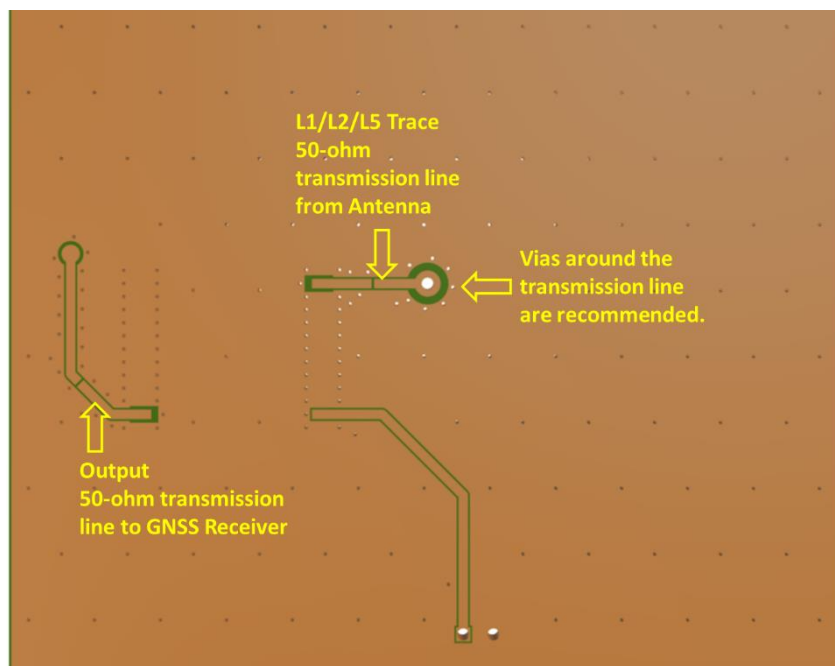
Top and bottom view of PCB.

4.2 Reference Design Integration

The TFM.110A should be placed as close to the signal input and output as possible to shorten the length of the transmission lines. The RF IN/OUT traces must maintain a 50 Ohm transmission line. A Pi Matching Network is recommended for the RF IN transmission lines, the values and components for the matching circuit will depend on the tuning needed. Ground vias should be placed beside each ground pad and the DC Voltage input should be between +1.8 and +5.5 VDC. It's recommended that the DC Voltage input should be coupled with a 100pF Capacitor and an ESD Diode.



Bottom view of the PCB, showing "Pi" matching network.



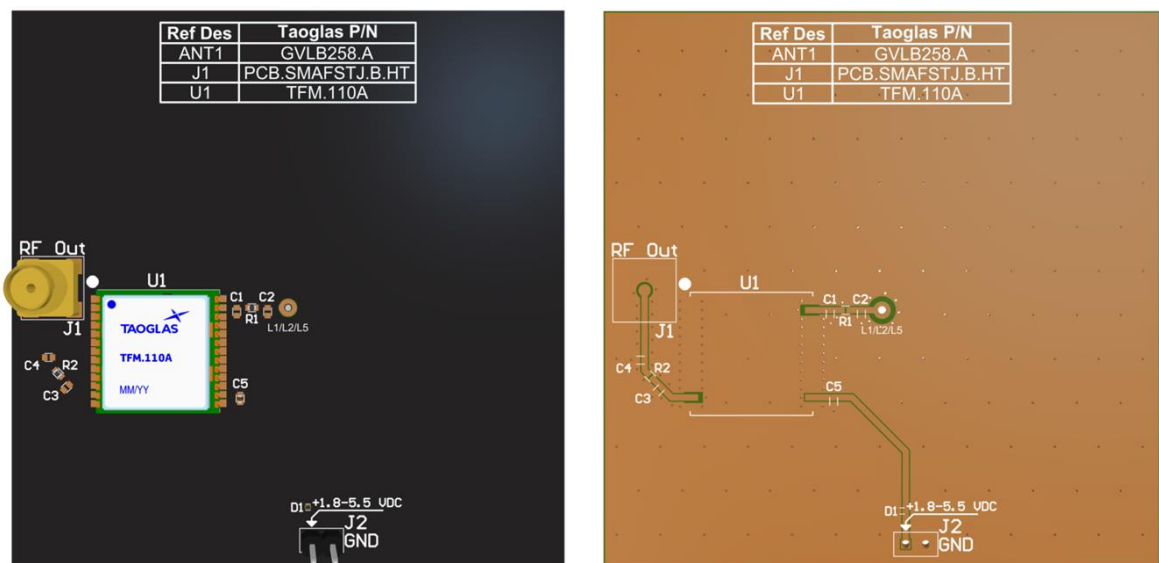
Bottom view of the PCB, showing transmission lines and integration notes.

4.3 Final Integration

The bottom side image shown below highlights the antenna connection to the TFM.110A module. It demonstrates the output of the TFM.110A module that needs to be connected to a GNSS receiver input. It displays the DC connection required with ESD diode and decoupling capacitor. Taoglas recommends using a minimum of 70x70mm ground plane (PCB) to ensure optimal performance.



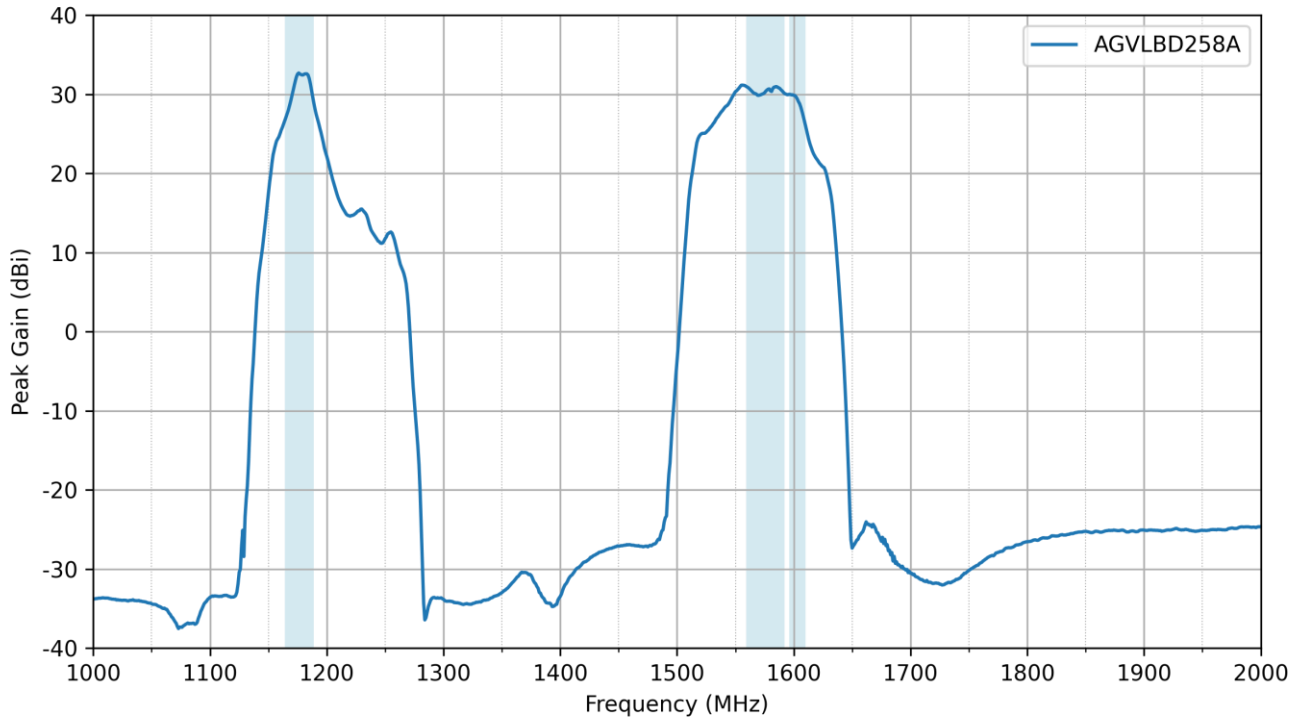
Top Side ([GVLB258.A](#) placement on 70x70mm PCB)



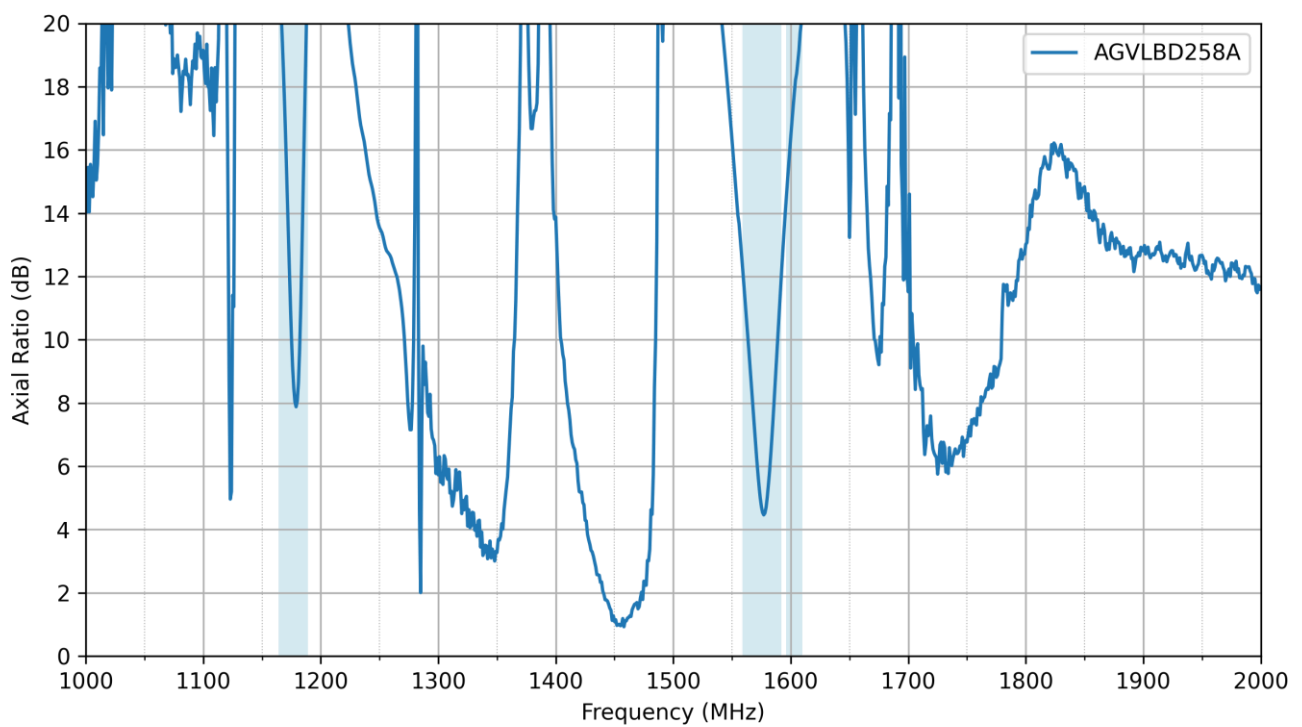
Bottom side ([TFM.110A](#) placement)

5. Antenna Characteristics

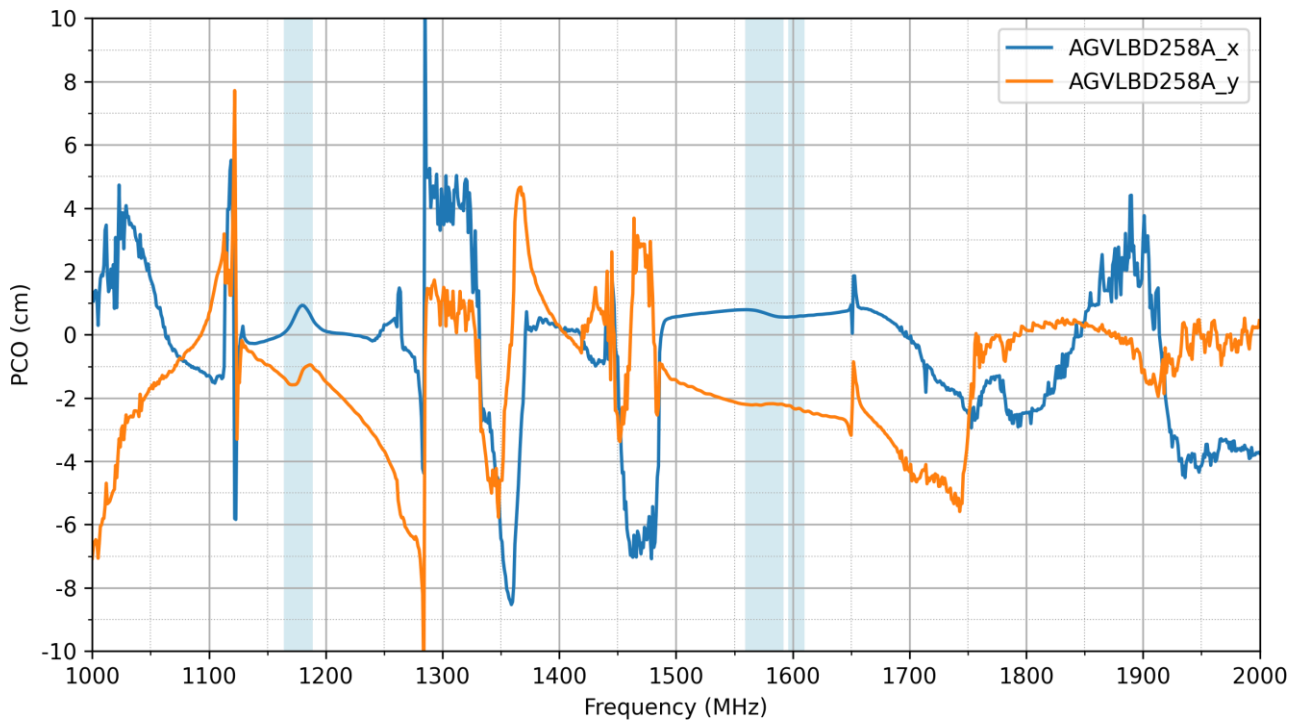
5.1 Peak Gain



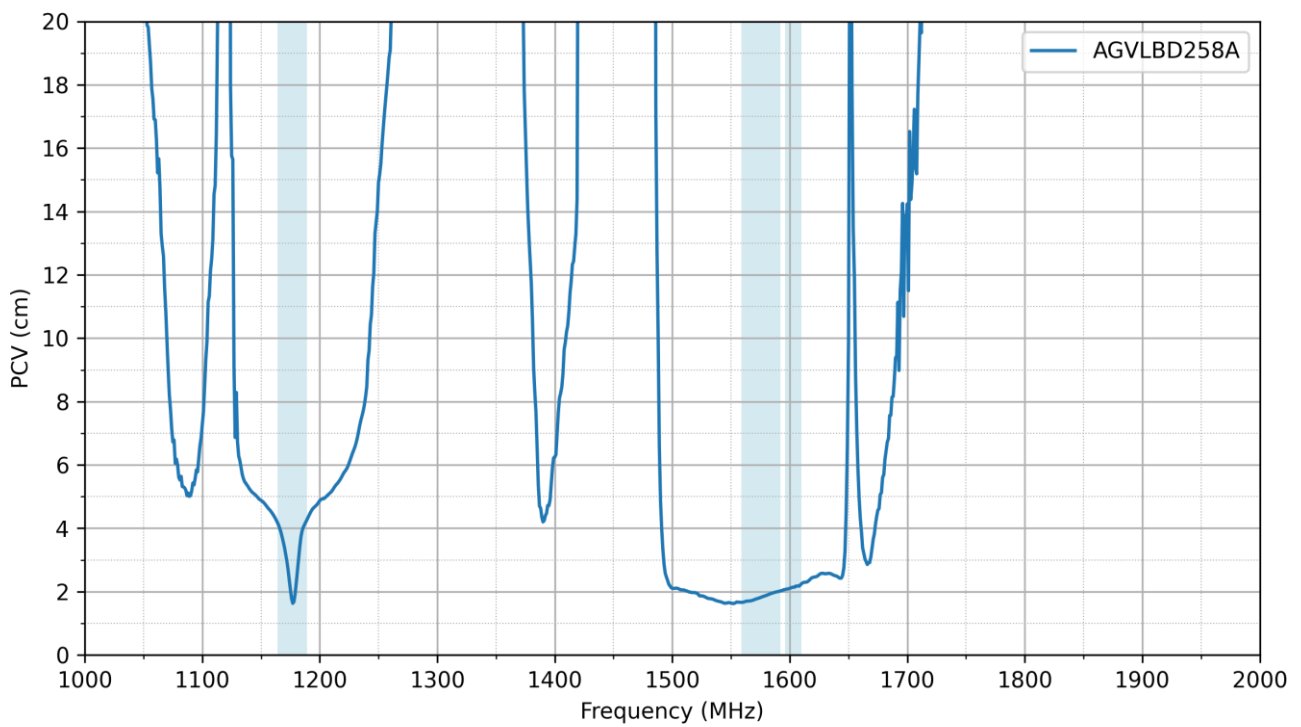
5.2 Axial Ratio



5.3 PCO

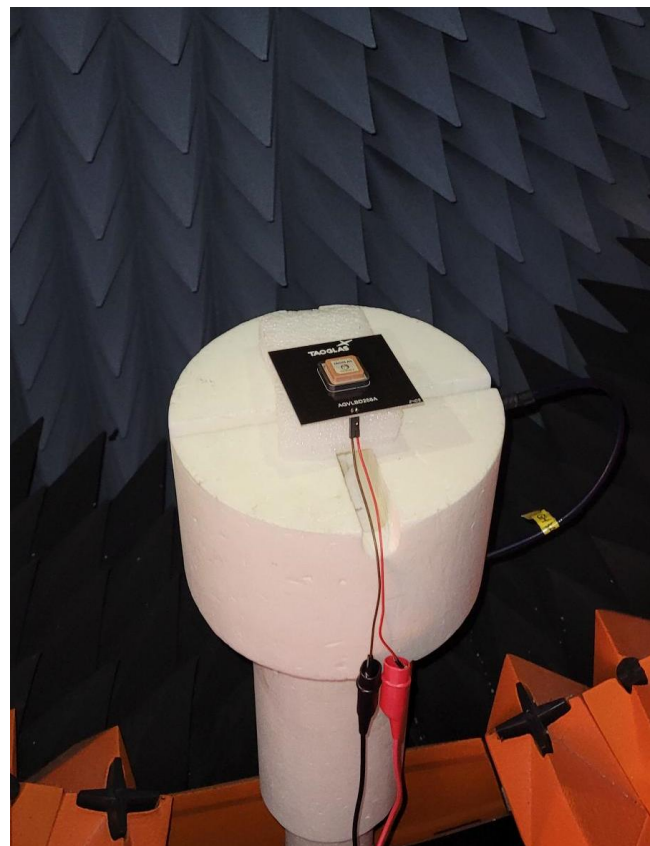
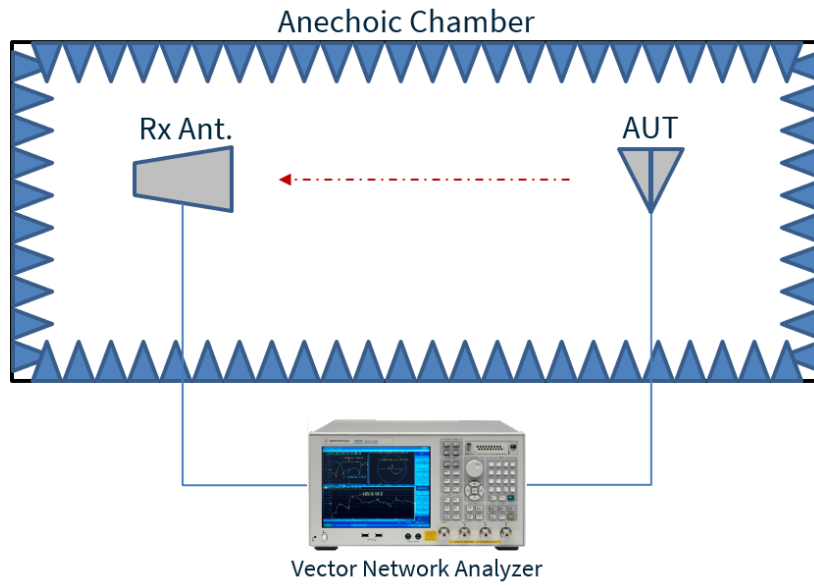


5.4 PCV



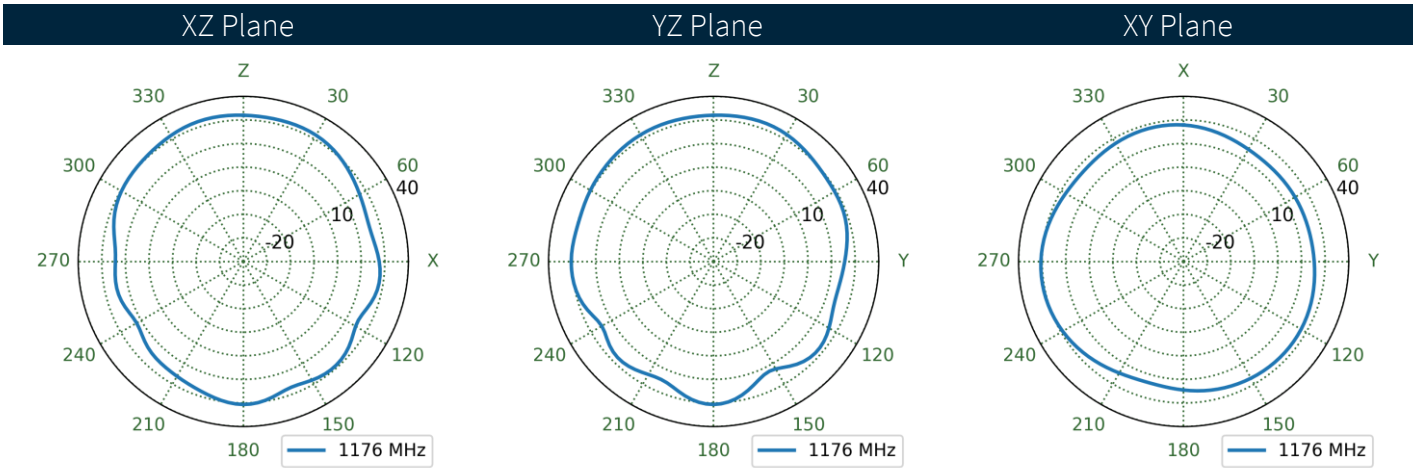
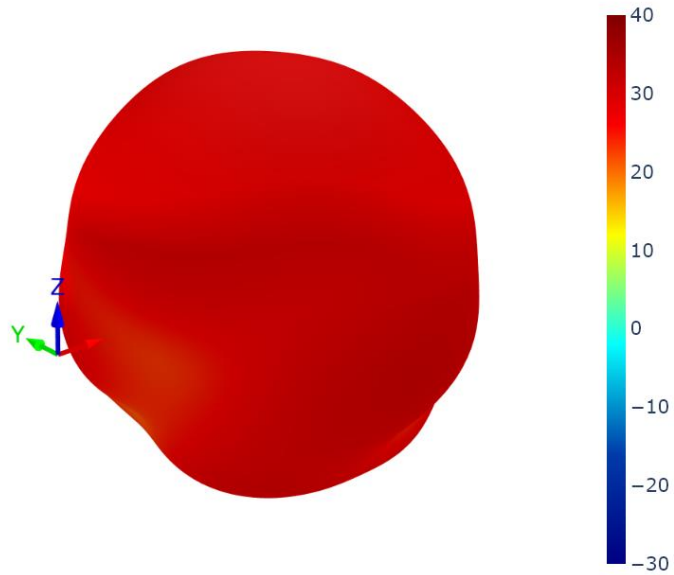
6. Radiation Patterns

6.1 Test Setup

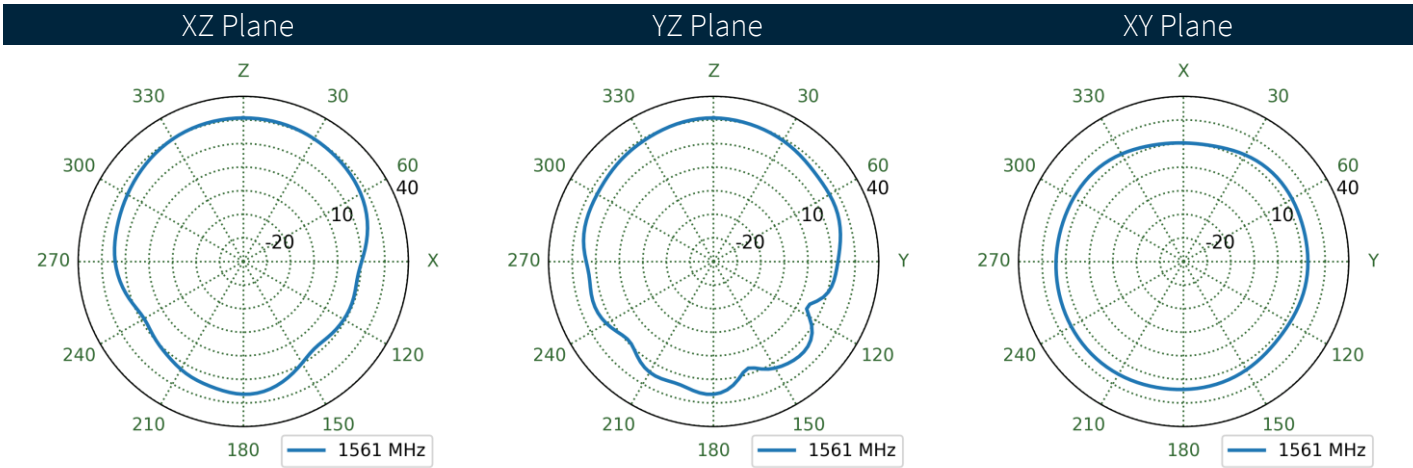
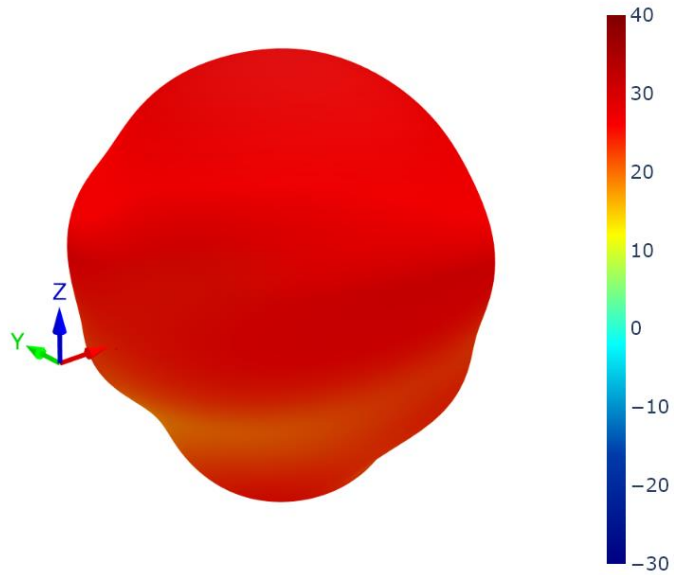


Chamber Test Set-up

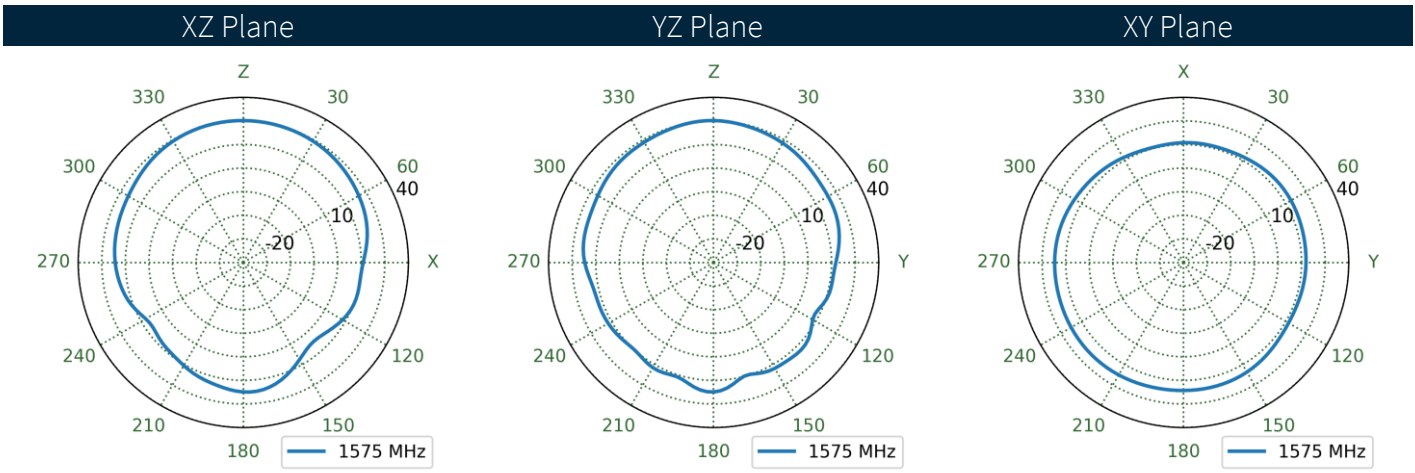
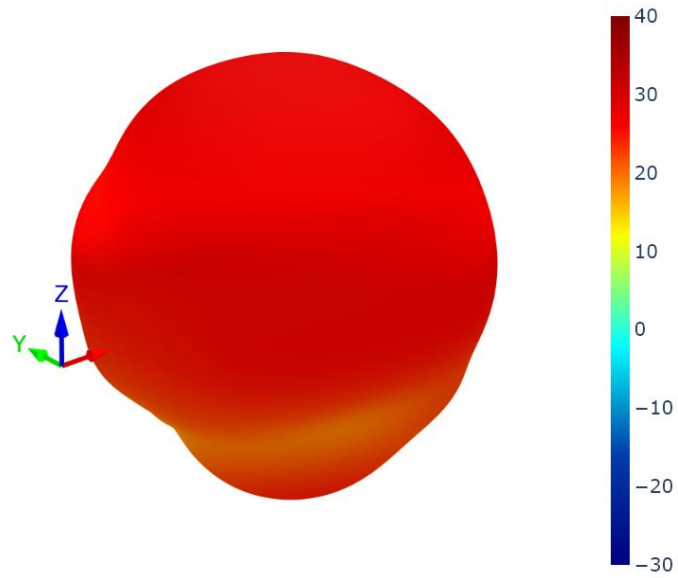
6.2 Patterns at 1176 MHz



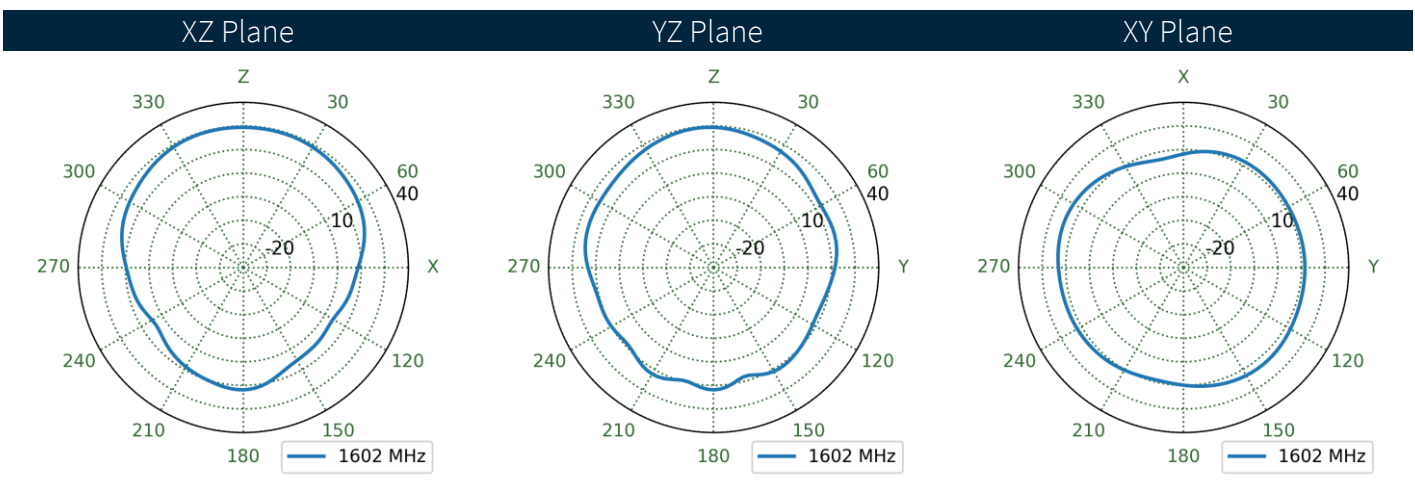
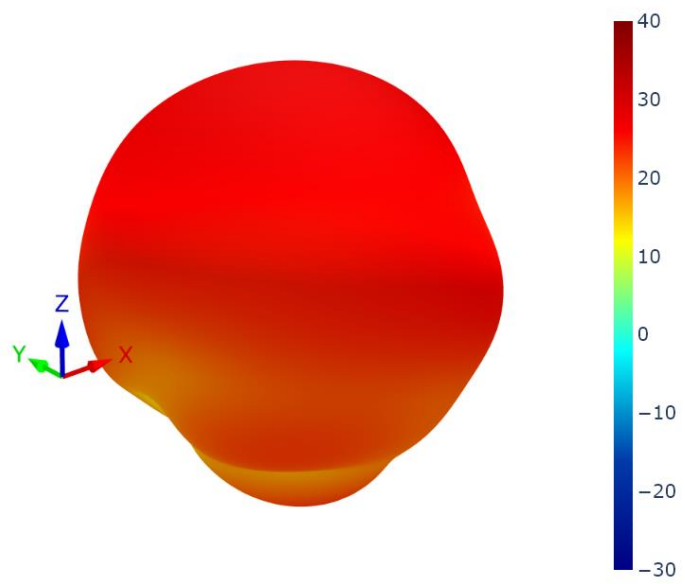
6.3 Patterns at 1561 MHz



6.4 Patterns at 1575 MHz



6.5 Patterns at 1602 MHz



Changelog for the datasheet

SPE-25-8-014 – AGVLBD258A

Revision: A (Original First Release)

Date:	2025-01-15
Notes:	Initial Release
Author:	Gary West

Previous Revisions



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