

TAOGLAS TFM.115A OA2A

Datasheet

GNSS Front End Module covering L1+B1+G1 / L5 and L-band

Part No: TFM.115A

Description

Surface mount GNSS front-end module covering L1+B1+G1/L5+L-banc

Features:

Two Stage LNA and SAW Filter Design Vin = +1.8 to +5.5 VDC Compact, easy to integrate surface-mount solution Dimensions: 20 x 18 x 2.75mm RoHS & Reach Compliant



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Changelog

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Introduction





The Taoglas TFM.115A is a surface-mount GNSS front-end which covers L1+B1+G1/L5 and L-band for multiband high-precision applications that require the full spectrum of GNSS constellations. The TFM.115A is a dual input single output and features a SAW/LNA/SAW/LNA topology in both the low and high band signal paths to prevent unwanted out-of-band interference from overdriving the GNSS LNAs or receiver. The SAW filters have been carefully selected and placed to provide excellent out-of-band rejection while also maintaining low noise figure.

Many currently available dual-band GNSS receivers require additional RF circuits between the antenna and the receiver to properly set the overall system noise figure. This requires additional development time for an otherwise simple module integration. Many organizations don't have the RF expertise to effectively design such a solution. The TFM.115A captures the required additional RF circuits in modular form, allowing the designer to simply place the TFM.115A between their GNSS antenna and GNSS receiver.

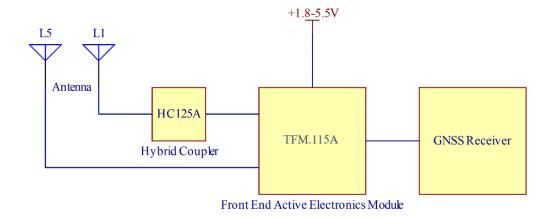
The TFM.115A offers > 25 dB gain across all applicable bands while maintaining a high Input P1dB of-25 dBm or better. Noise Figure is < 3.5 dB in the low bands and < 4.0 dB in the high bands. A wide input voltage of +1.8 to +5.5 VDC allows for easy integration in most GNSS systems.

TFM.115A Features and Benefits:

- Ease-of-integration Single-package solution combines impedance matching, filter efficiency and low noise design for easy, drop-in use with any antenna or GNSS receiver
- Low-noise System Design Integrated pre-filters deliver exceptional out-of-band rejection across multiple band configurations and neighboring interference to properly set noise figure
- Dual-gain Stage Architecture Cascaded LNAs, pre-filters and optimized impedance matching deliver sufficient gain to the GNSS receiver without signal-to-noise overload
- Low-profile Form Factor Small footprint and low-profile design saves valuable real estate without the need for external components and routing
- Accelerated Development Cycles 2+ years of development by antenna and RF design experts, delivering the highest levels of integration, manufacturability and robustness in a single package

For further information, please contact your regional Taoglas customer support team.





Block diagram of the integration for the TFM.115A.

We used the <u>HP54510A</u> to demonstrate the integration of this module but please note that we have other compatible antennas that can also be used alongside the TFM.115A please see table below.

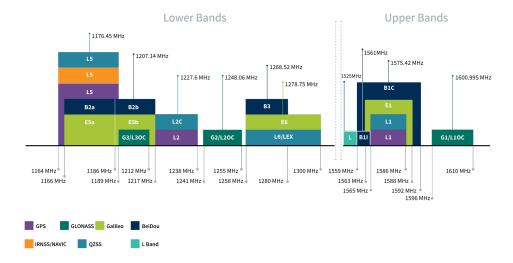
| Compatible Antennas |
|---------------------|
| <u>HP54510A</u> |
| <u>HP5354.A</u> |

Taoglas also offers the TFM.112A for L1 /L2+L-Band applications. View the full series of TFM modules here



2. Specification

| | | GNSS Frequ | iency Bands | | |
|------------------|-------------------------|---------------------------|--------------------|--------------------|-------------------|
| GPS | L1 1575.42 MHz | L2 1227.6 MHz | L5 1176.45 MHz | | |
| | - | | | | |
| GLONASS | G1 1602 MHz | G2 1248 MHz | G3 1207 MHz | | |
| | - | | | | |
| Galileo | E1 1575.24 MHz | E5a 1176.45 MHz | E5b 1201.5 MHz | E6 1278.75 MHz | |
| | - | - | | | |
| BeiDou | B1C 1575.42 MHz | B1I 1561 MHz | B2a 1176.45 MHz | B2b 1207.14 MHz | B3 1268.52 MHz |
| | - | | - | | |
| L-Band | L-Band 1542 MHz | | | | |
| | - | | | | |
| QZSS (Regional) | L1 1575.42 MHz | L2C 1227.6 MHz | L5 1176.45 MHz | L6 1278.75e6 | |
| | - | | | | |
| IRNSS (Regional) | L5 1176.45 MHz | | | | |
| | | | | | |
| SBAS | L1/E1/B1 1575.42 MHz | L5/B2a/E5a 1176.45 MHz | G1 1602 MHz | G2 1248 MHz | G3 1207 MHz |
| | - | - | - | | |



GNSS Bands and Constellations



| Electrical@ | | | | | |
|-------------------------|------------------|-------|-------|---------|-------|
| Frequency (MHz) | 1176 | 1542 | 1561 | 1575.42 | 1602 |
| Noise Figure (dB)* | 2.6 | 2.5 | 2.6 | 2.3 | 2.5 |
| Gain (dB) | 28.8 | 27.1 | 28.2 | 27.9 | 26.3 |
| Group Delay (ns) | 35.2 | 17.2 | 16.4 | 16.3 | 19.9 |
| Input Return Loss (dB) | -24.9 | -15.9 | -23.4 | -12.7 | -14.6 |
| Output Return Loss | -6.1 | -5.9 | -6.9 | -6.0 | -5.6 |
| Vin | +1.8 to +5.5 VDC | | | | |
| Typical Current (@1.8V) | 7.5 – 9.0mA | | | | |

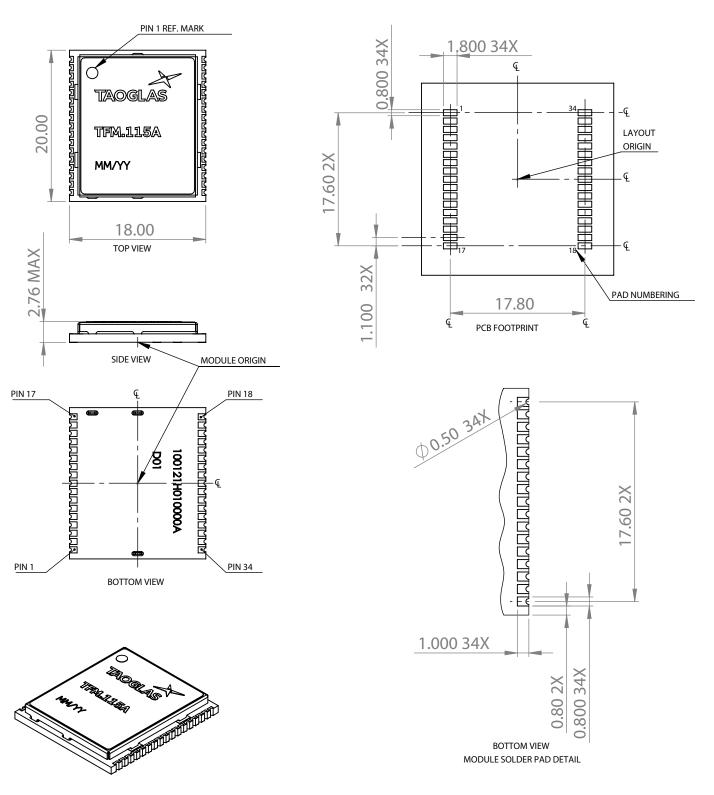
*Note: Tested on an evaluation board. Board losses removed.

| Mechanical | | |
|------------------|--------------|--|
| Height | 2.76 mm max. | |
| Planar Dimension | 20 x 18 mm | |
| Weight | 2g | |

| Environmental | | | | |
|----------------------------------|---------------|--|--|--|
| Temperature Range | -40°C to 85°C | | | |
| RoHS Compliant | Yes | | | |
| REACH Compliant | Yes | | | |
| Moisture Sensitivity Level (MSL) | 3 | | | |



3.



ISO VIEW

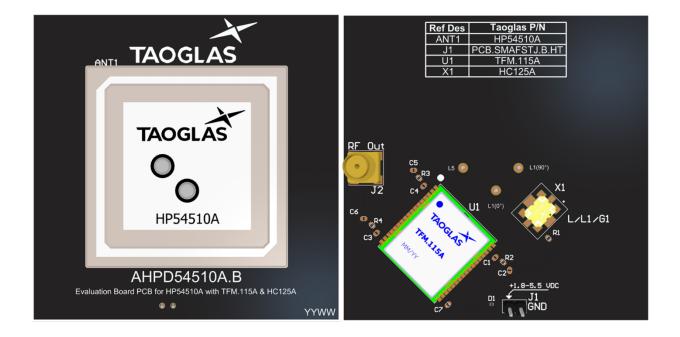




Module Integration

The following is an example on how to integrate the TFM.115A into a design. In this example, the <u>HP54510A</u> (L1/L5) is used as the antenna. This antenna has three pins, two pins are used for L1 band, and the other pin is used for the L5 band. A Hybrid Coupler (<u>HC125A</u>) is used to combine the feeds for the L1 band, to create a Right hand circular polarized (RHCP) signal, before being presented to the corresponding inputs on the TFM.115A.

The TFM.115A is powered from a separate power DC supply (1.8V-5.5V). The output of the TFM.115A can then be fed to a relevant GNSS receiver module. Taoglas recommends using a minimum of 70x70mm ground plane (PCB) to ensure optimal performance.



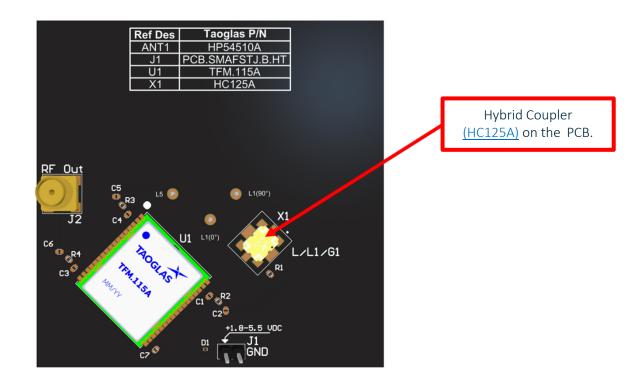
Top and bottom view of PCB.

Please find the Integration files in Altium, 2D formats and the 3D model for the TFM.115A on the product page here.

https://www.taoglas.com/product/tfm-115a-gnss-front-end-module-covering-l1b1g1-l5l-band/



Schematic Symbol and Pin Definitions



Above are the 3D models of the TFM.115A and <u>HC125A</u> on the PCB.

The circuit symbol for the TFM.115A is shown below. The module has 34 pins as indicated below.

| Pin | Description |
|---------------------------|------------------|
| 1, 3-15, 17-18, 20-32, 34 | Ground |
| 2 | L5 Input |
| 16 | RF Signal Output |
| 19 | Voltage Input |
| 33 | L1 Input |

| | TAOGLAS Ul | S_TFM.115 | A |
|--|---|---|---|
| $ \begin{array}{c} 1\\ 2\\ 3\\ 4\\ 5\\ 6\\ 7\\ 8\\ 9\\ 10\\ 11\\ 12\\ 13\\ 14\\ 15\\ 16\\ 17\\ \end{array} $ | GND L5 IN GND GND GND GND GND GND GND GND GND GN | GND L1 IN GND GND GND GND GND GND GND GND GND GN | 34 33 32 31 30 29 28 27 25 24 23 22 21 20 19 18 |
| | | | |

Above is a schematic symbol of TFM.115A and a table of the pin definitions.

4.1



4.2 Schematic Layout

The <u>HP54510A</u> uses two orthogonal feeds that need to be combined in a hybrid coupler to ensure optimal axial ratio and RHCP Gain is achieved. Taoglas recommends our <u>HC125A</u>, a high-performance hybrid coupler specifically engineered for use with our multi feed patches.

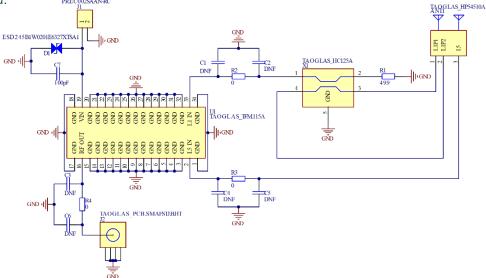
The <u>HC125A</u> is required for the high GNSS band of operation (1559-1610MHz) for this antenna. This hybrid coupler should be placed close to the antenna pins and terminated correctly using two 100 Ohm resistors in parallel.

The output of the hybrid coupler can be fed into the relevant input of the TFM.100B module.

Matching components with the TFM.115A are required for the module to have optimal performance in the spaces specified in the schematic below. Additional matching components may be necessary for your device, Taoglas recommends incorporating extra component footprints, forming a "pi" network, between the TFM.115A and the <u>HC125A</u>. Matching components should also be placed between the RF output pin and the GNSS receiver module input pin.

Taoglas recommends placing an ESD diode and decoupling capacitor (100pF) on the input pin of the supply rail.

Note: The RF In & RF out of the TFM module are all DC-blocked internally. External DC block capacitors are notrequired.



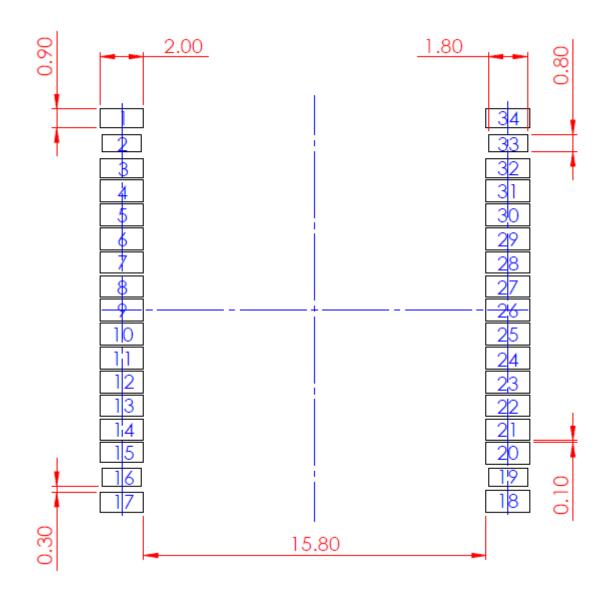
Schematic above shows how the TFM.115A and <u>HP54510A</u> are integrated.

| Designator | Туре | Value | Manufacturer | Manufacturer Part Number |
|------------------------|-----------|------------|--------------|--------------------------|
| C1, C2, C3, C4, C5, C6 | Capacitor | Not Fitted | - | - |
| C7 | Capacitor | 100pF | Murata | GRM1555C1H101JA01D |
| D1 | Diode | - | Infineon | ESD245B1W0201E6327XTSA1 |
| R1 | Resistor | 49.9 Ohms | Panasonic | ERJ-2RKF49R9X |
| R2, R3, R4 | Resistor | 0 Ohms | YAGEO | RC0402JR-070RL |

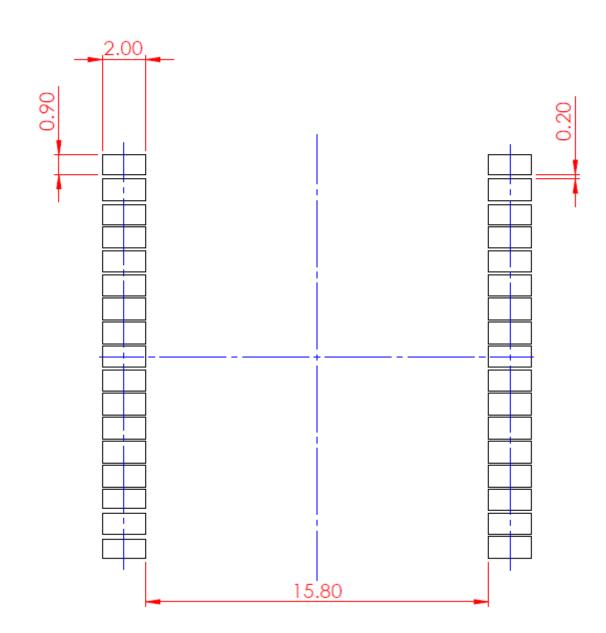
g



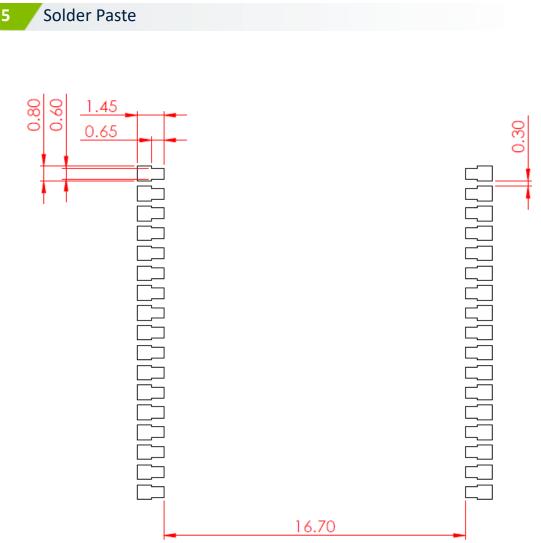
4.3 Module Footprint









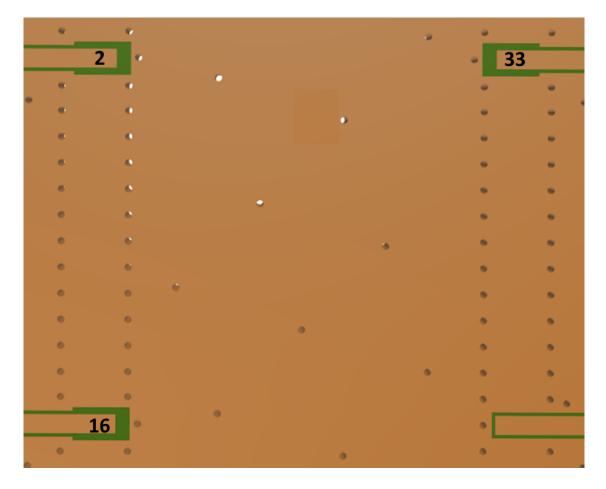




4.6 Copper Clearance

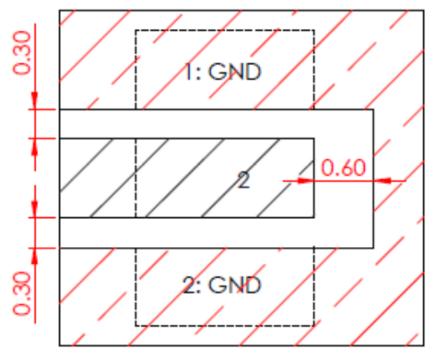
The footprint and clearance on the PCB must comply with the front-end module's specification. The PCB layout shown in the diagrams below demonstrates the TFM.115A clearance area for Pin 16 (RF OUT Pad) and Pin 33 (L1 IN Pad). This clearance also applies to Pin 2 (L5 IN Pad). The copper keep out area only applies to the same layer that the TFM.115A has been placed on.

There should be 0.3mm copper clearance between the feed pad and ground pads with at least a 0.6mm copper clearance from the ground plane.

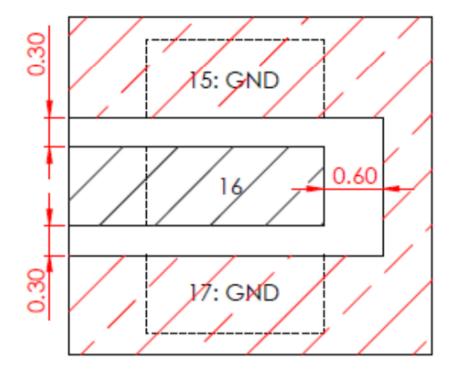


3D Image of Copper Clearances for TFM.115A.



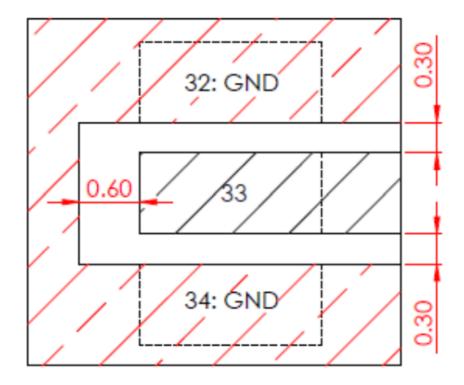


Copper Clearance for Pin 2 (L2 IN Pad) of the TFM.115A.



Copper Clearance for Pin 16 (RF OUT Pad) of the TFM.115A.





Copper Clearance for Pin 33 (L1 IN Pad) of the TFM.115A.



4.7 Module Integration

The TFM.115A should be placed as close to the signal input and output as possible to shorten the length of the transmission lines. The RF IN/OUT traces must maintain a 50 Ohm transmission line. A Pi Matching Network is recommended for the RF IN transmission lines, the values and components for the matching circuit will depend on the tuning needed. Ground vias should be placed beside each ground pad and the DC Voltage input should be between +1.8 and +5.5 VDC. It's recommended that the DC Voltage input should be coupled with a 100pF Capacitor and an ESD Diode.



TFM.115A module mounted on a PCB, showing transmission lines and integration notes.

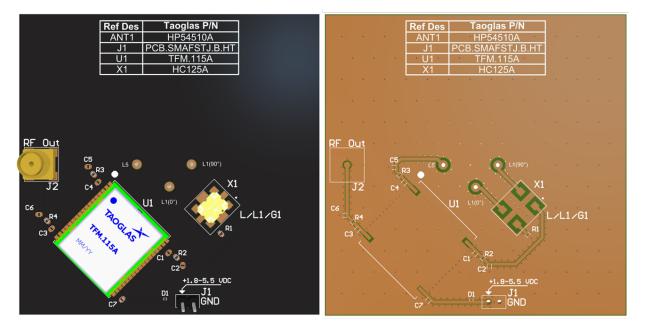


4.8 Final Integration

The bottom side image shown below highlights the antenna connection to the hybrid coupler (HC125A). It highlights the outputs of the hybrid couplers connected to the relevant inputs of the TFM.115A module. It shows the 49.9 Ohm terminating resistor necessary for the hybrid coupler (HC125A). It also demonstrates the output of the TFM.115A module that needs to be connected to a GNSS receiver input. It displays the DC connection required with ESD diode and decoupling capacitor. Taoglas recommends using a minimum of 70x70mm ground plane (PCB) to ensure optimal performance.



Top Side (HP54510A placement on 70x70mm PCB)



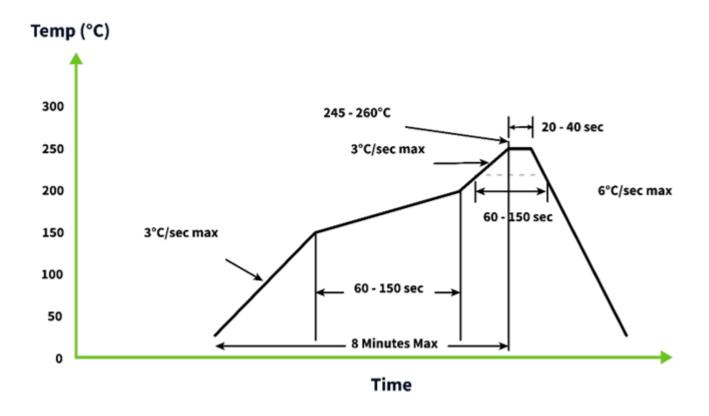
Bottom side (TFM.115A placement including <u>HC125A's</u>)



Solder Recommendations

5.

The TFM.115A can be assembled by following the recommended soldering temperatures as follows:



Smaller components are typically mounted on the first pass, however, we do advise mounting the TFM.115A when placing larger components on the board during subsequent reflows.



Packaging

600pcs per tape and reel 1 pcs humidity indicator card 2 pcs desiccant 3g

600pcs per vacuum bag

600pcs per box Box dimensions: 350 x 340 x 67mm Weight: 2Kg





2400pcs per carton Box dimensions: 370 x 360 x 275mm Weight: 8.8Kg

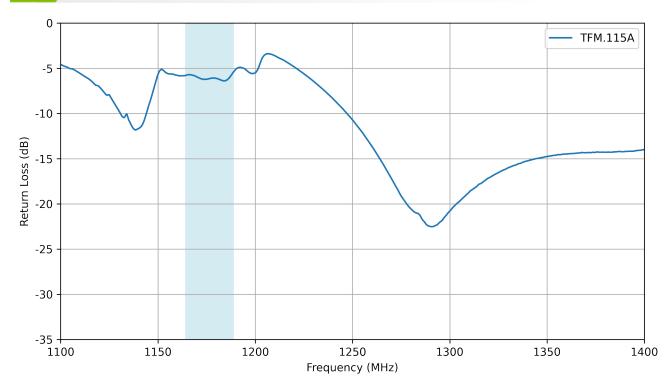




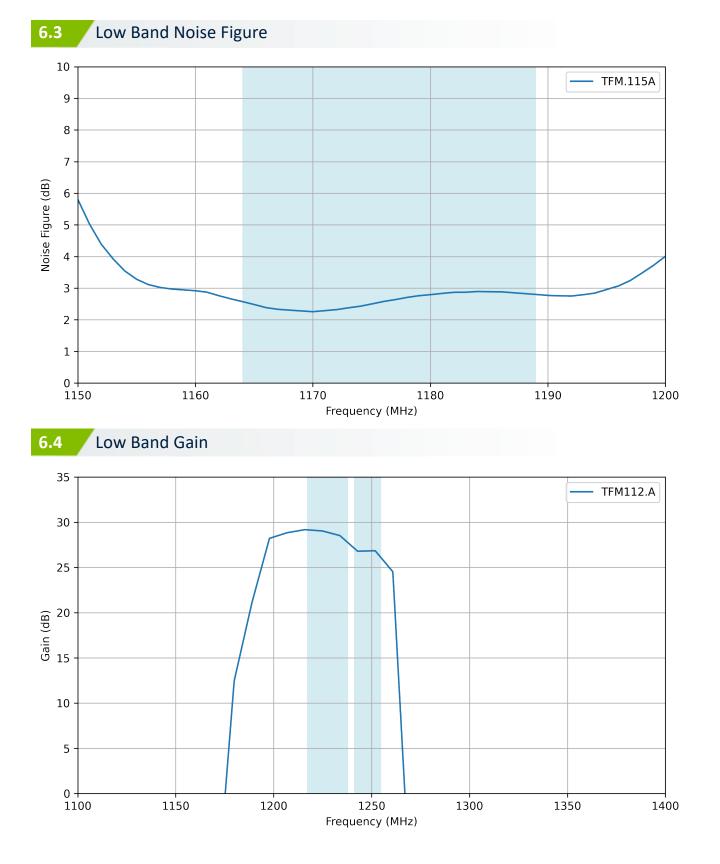
6. FEM Low Band Characteristics



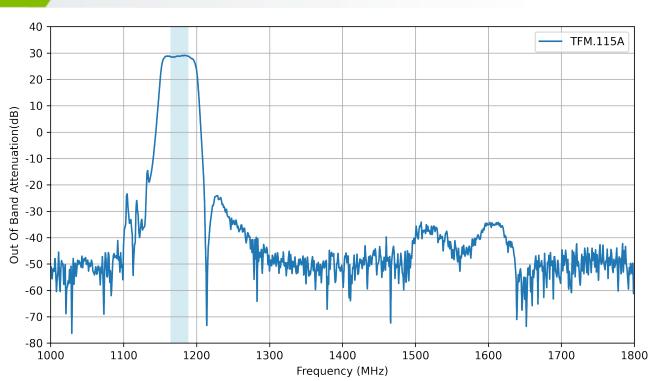








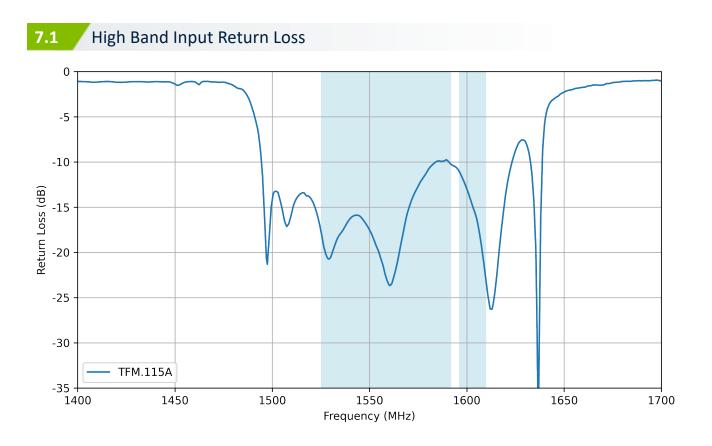


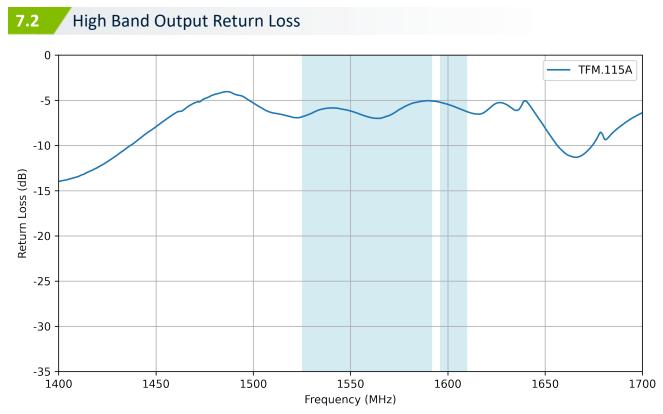


6.5 Low Band Gain and Attenuation



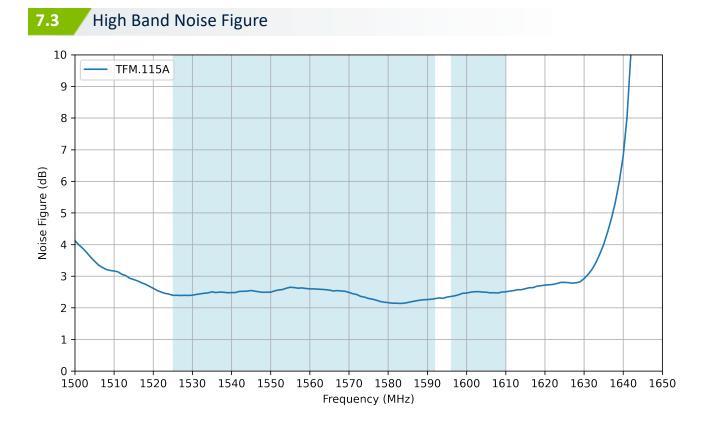
. FEM High Band Characteristics

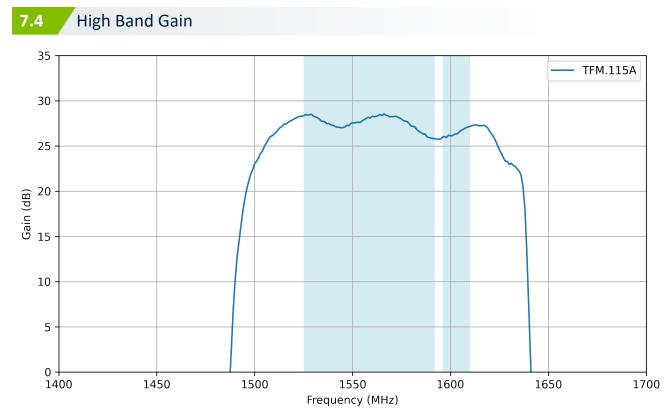




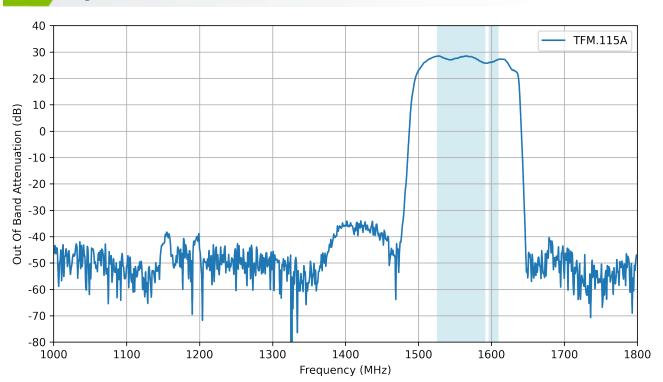
7.











7.5 High Band Gain and Attenuation



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|-------------------------|-----------------------------|--|--|--|
| SPE-24-8-248 – TFM115.A | | | | |
| Revision: A (Original | | | | |
| Date: | 2024-10-02 | | | |
| Notes: | Initial Release | | | |
| Author: | Gary West | | | |
| | | | | |

Previous Revisions





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